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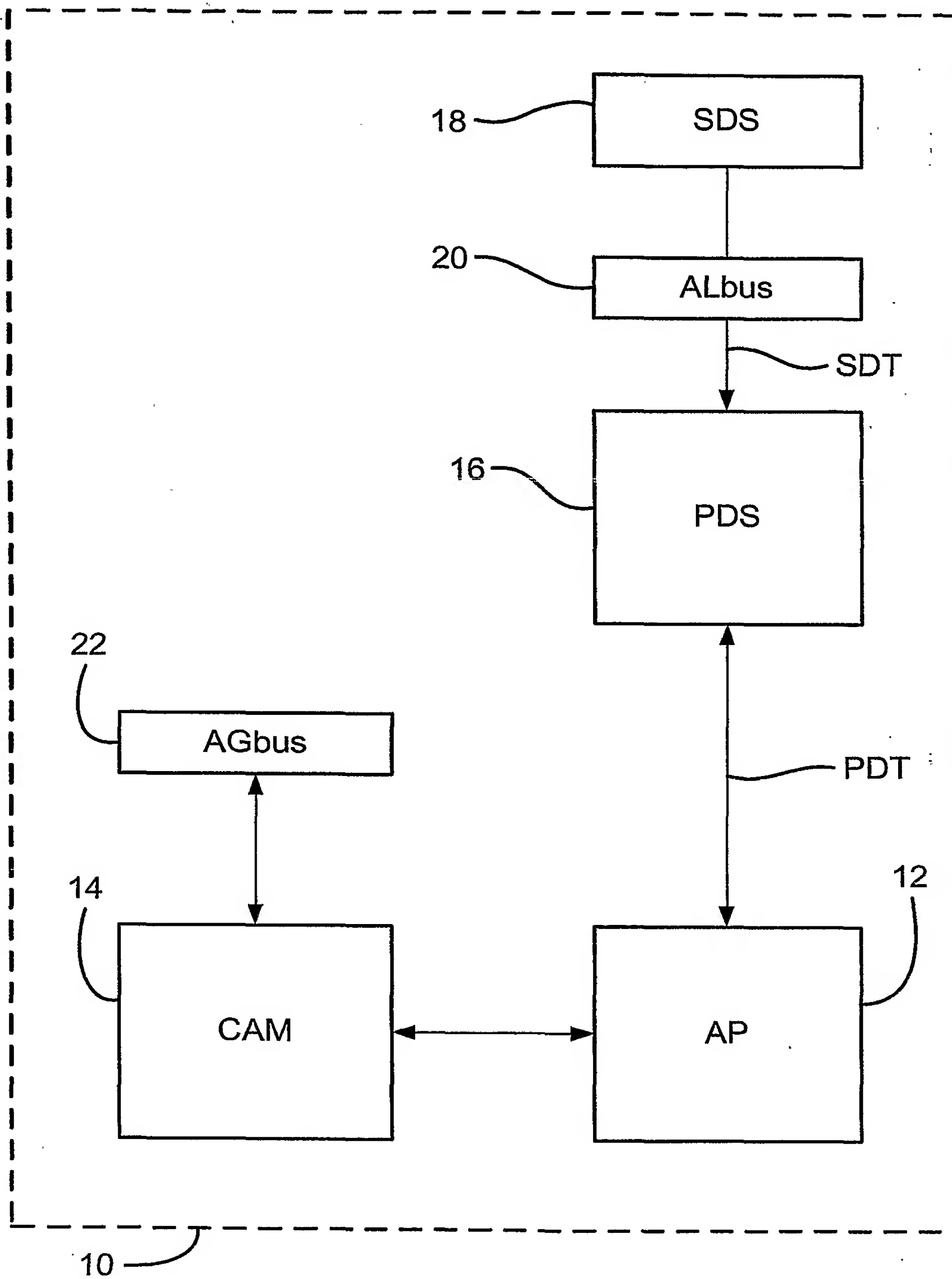
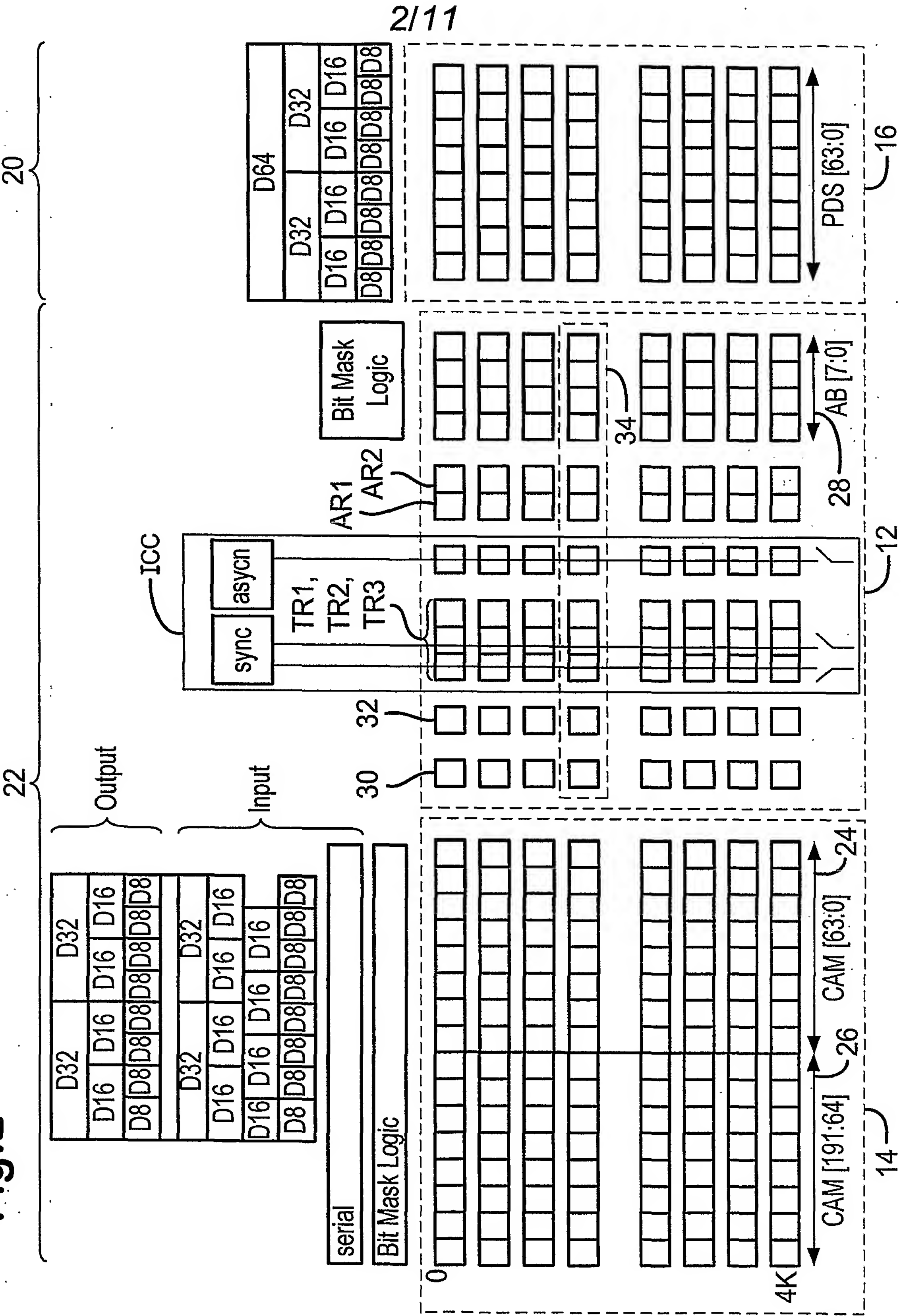
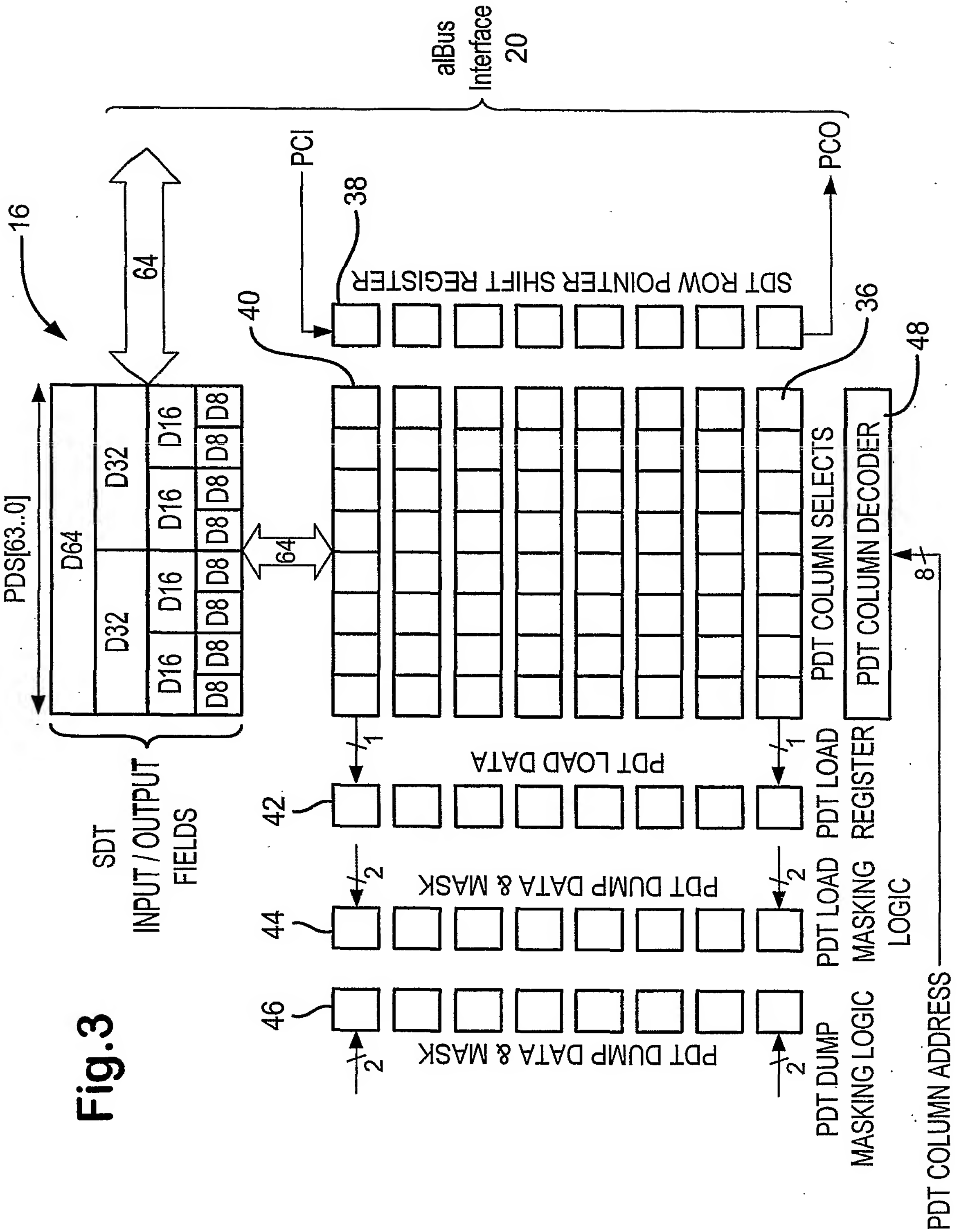
**Fig.1**

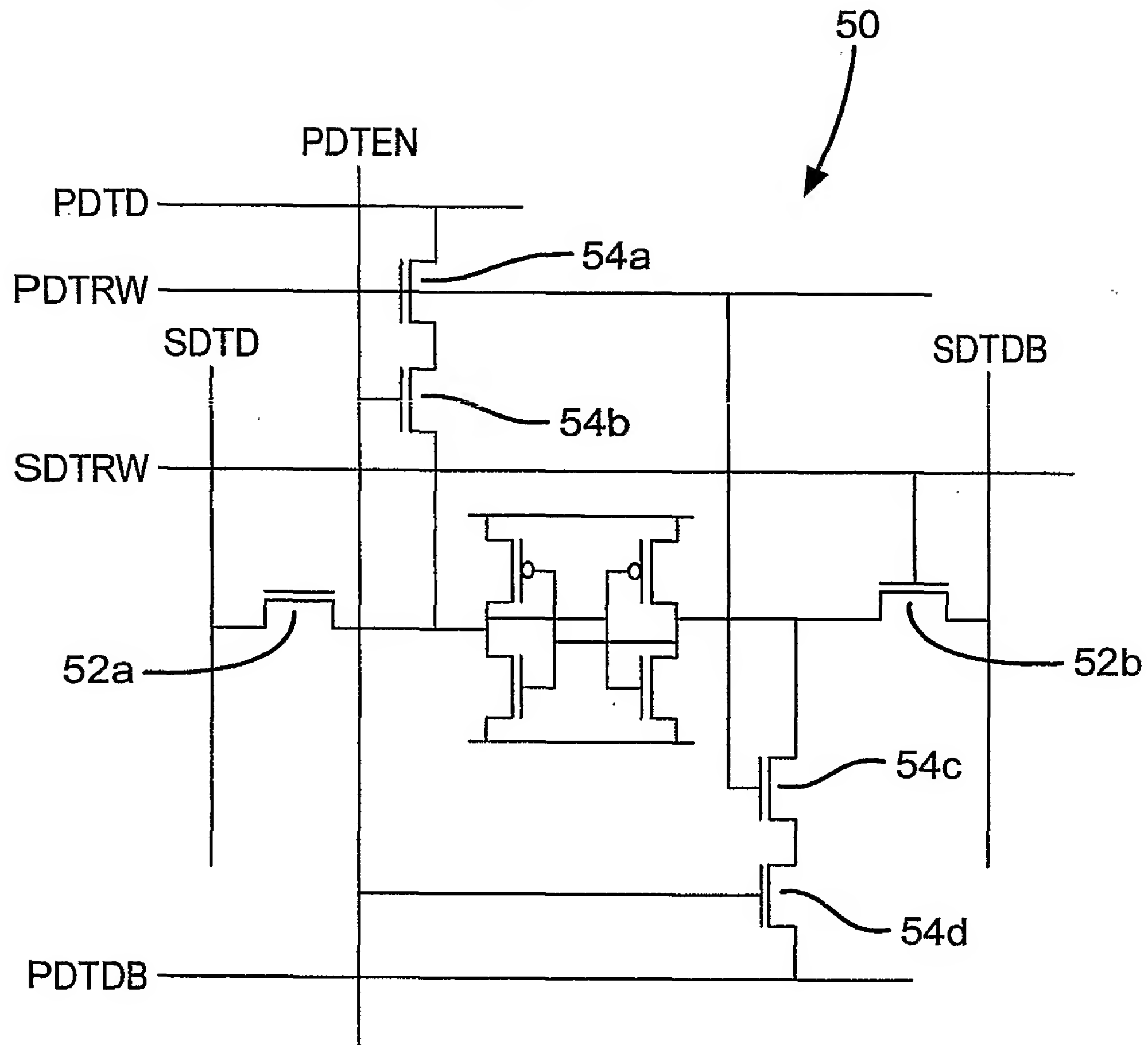
Fig.2





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**Fig.4**



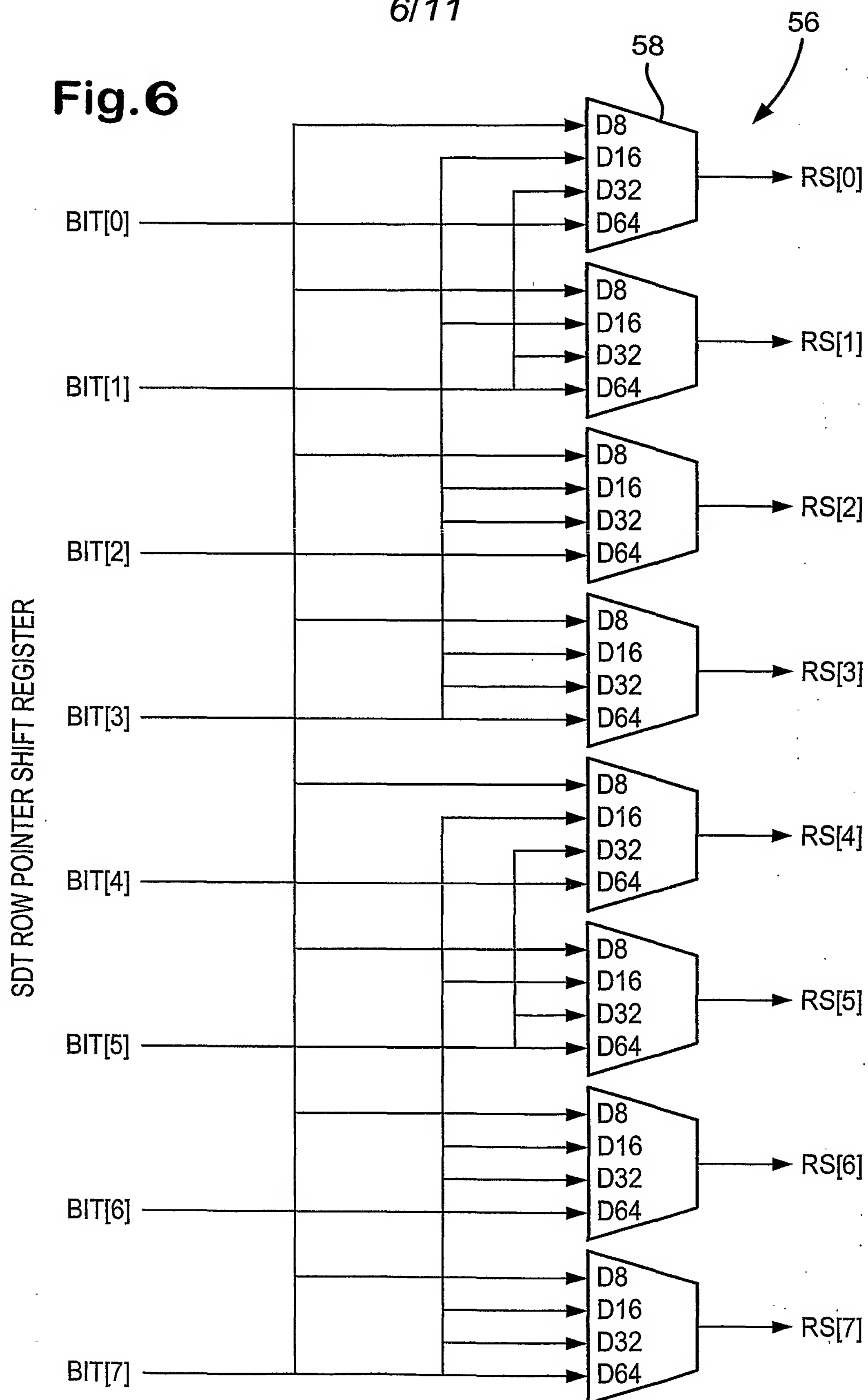
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**Fig.5**

row address MOD 8	Row Strobe logic conditions
0	$SDTRW\_A[0] = RS[0]$
	$SDTRW\_B[0] = \sim D8.RS[0]$
	$SDTRW\_C[0] = (D32+D64).RS[0]$
	$SDTRW\_D[0] = D64.RS[0]$
1	$SDTRW\_A[1] = (D8+D64).RS[1]$
	$SDTRW\_B[1] = (D16+D64).RS[1]$
	$SDTRW\_C[1] = (D32+D64).RS[1]$
	$SDTRW\_D[1] = D64.RS[1]$
2	$SDTRW\_A[2] = \sim D16.RS[2]$
	$SDTRW\_B[2] = (D16+D64).RS[2]$
	$SDTRW\_C[2] = (D32+D64).RS[2]$
	$SDTRW\_D[2] = D64.RS[2]$
3	$SDTRW\_A[3] = (D8+D64).RS[3]$
	$SDTRW\_B[3] = \sim D8.RS[3]$
	$SDTRW\_C[3] = (D32+D64).RS[3]$
	$SDTRW\_D[3] = D64.RS[3]$
4	$SDTRW\_A[4] = (D8+D64).RS[4]$
	$SDTRW\_B[4] = \sim D8.RS[4]$
	$SDTRW\_C[4] = (D32+D64).RS[4]$
	$SDTRW\_D[4] = D64.RS[4]$
5	$SDTRW\_A[5] = \sim D16.RS[5]$
	$SDTRW\_B[5] = (D16+D64).RS[5]$
	$SDTRW\_C[5] = (D32+D64).RS[5]$
	$SDTRW\_D[5] = D64.RS[5]$
6	$SDTRW\_A[6] = (D8+D64).RS[6]$
	$SDTRW\_B[6] = (D16+D64).RS[6]$
	$SDTRW\_C[6] = (D32+D64).RS[6]$
	$SDTRW\_D[6] = D64.RS[6]$
7	$SDTRW\_A[7] = RS[7]$
	$SDTRW\_B[7] = \sim D8.RS[7]$
	$SDTRW\_C[7] = (D32+D64).RS[7]$
	$SDTRW\_D[7] = D64.RS[7]$

Table 2

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**Fig.6**

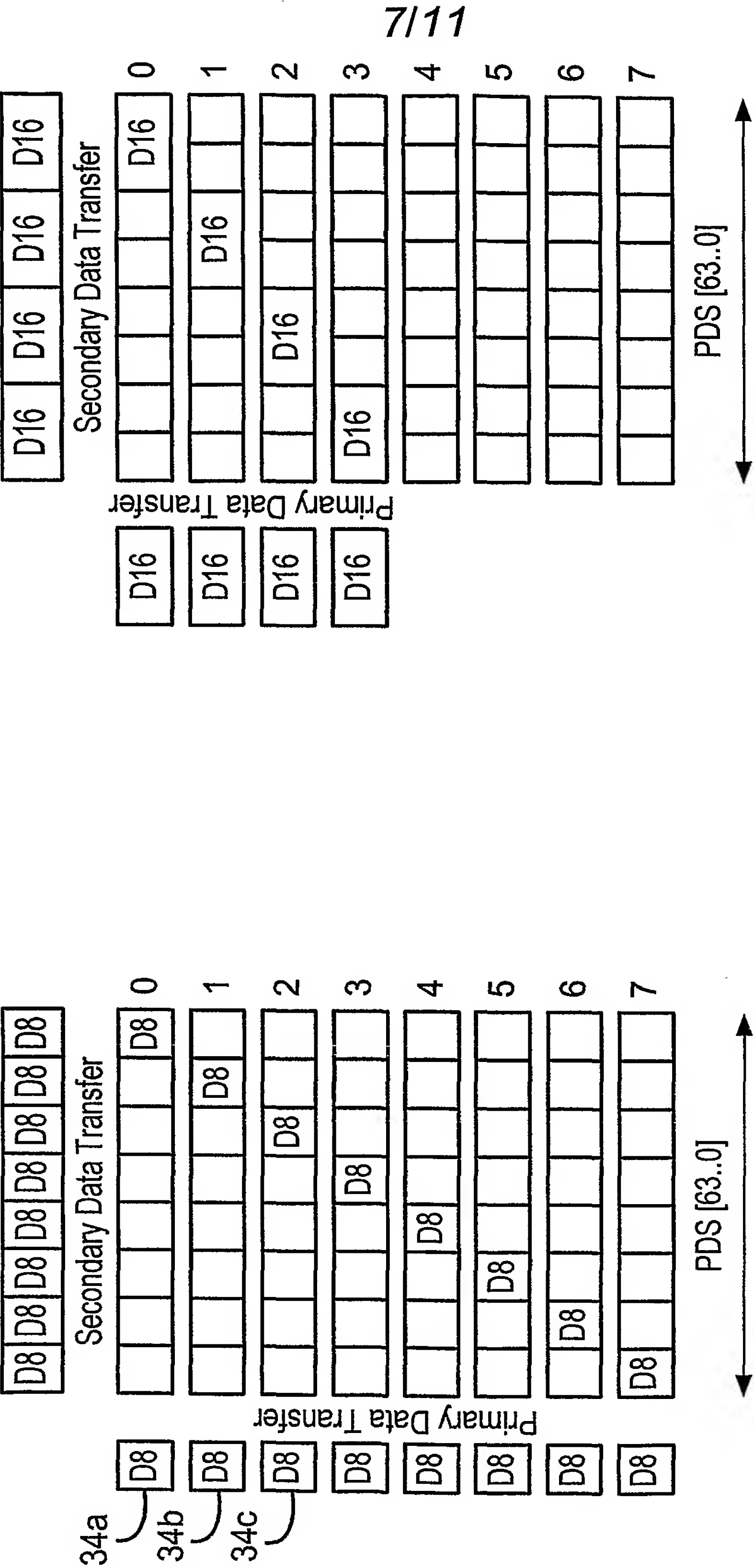


Fig. 7 (a)

Fig. 7 (b)

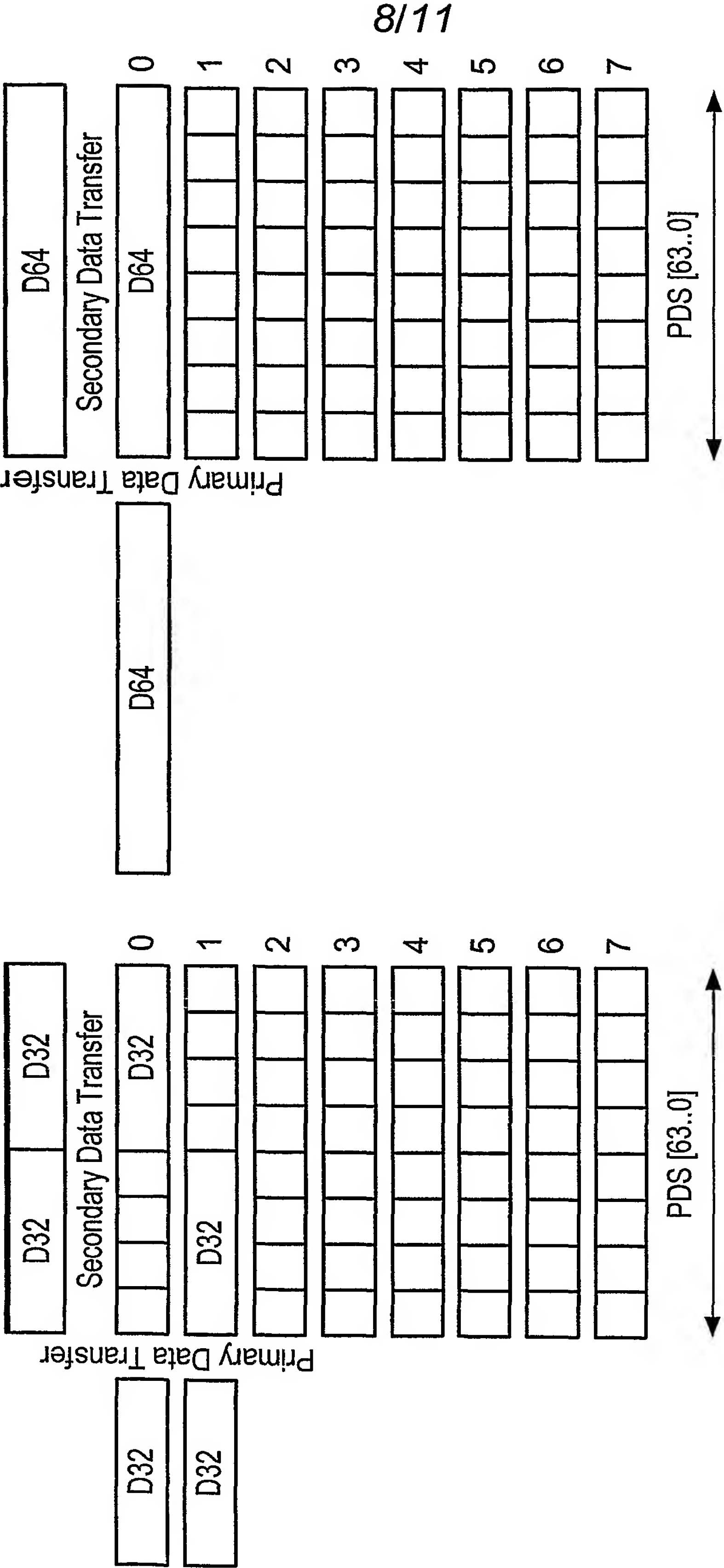


Fig.7 (c)

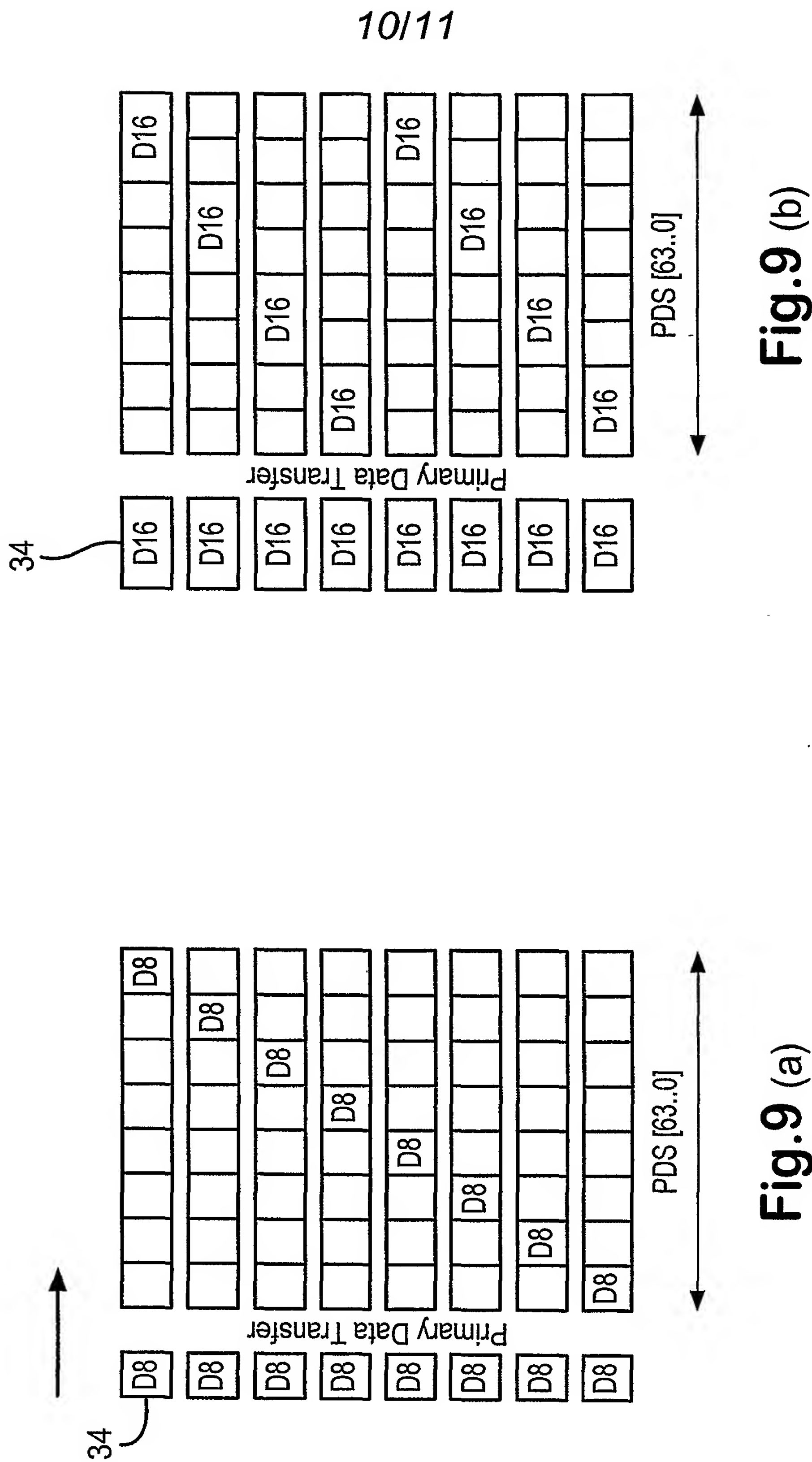
Fig.7 (d)



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**Fig.8**

byte address	column address	Row Strobe logic conditions
0	7..0	$PDTEN\_A[7..0] = BS[0].CS[7..0]$
		$PDTEN\_B[7..0] = \sim D8.BS[0].CS[7..0]$
		$PDTEN\_C[7..0] = (D32+D64).BS[0].CS[7..0]$
		$PDTEN\_D[7..0] = D64.BS[0].CS[7..0]$
1	15..8	$PDTEN\_A[15..8] = (D8+D64).BS[1].CS[15..8]$
		$PDTEN\_B[15..8] = \sim D8.BS[1].CS[15..8]$
		$PDTEN\_C[15..8] = (D32+D64).BS[1].CS[15..8]$
		$PDTEN\_D[15..8] = D64.BS[1].CS[15..8]$
2	24..16	$PDTEN\_A[24..16] = \sim D16.BS[2].CS[24..16]$
		$PDTEN\_B[24..16] = (D16+D64).BS[2].CS[24..16]$
		$PDTEN\_C[24..16] = (D32+D64).BS[2].CS[24..16]$
		$PDTEN\_D[24..16] = D64.BS[2].CS[24..16]$
3	31..25	$PDTEN\_A[31..25] = (D8+D64).BS[3].CS[31..25]$
		$PDTEN\_B[31..25] = \sim D8.BS[3].CS[31..25]$
		$PDTEN\_C[31..25] = (D32+D64).BS[3].CS[31..25]$
		$PDTEN\_D[31..25] = D64.BS[3].CS[31..25]$
4	39..32	$PDTEN\_A[31..25] = (D8+D64).BS[4].CS[39..32]$
		$PDTEN\_B[31..25] = \sim D8.BS[4].CS[39..32]$
		$PDTEN\_C[31..25] = (D32+D64).BS[4].CS[39..32]$
		$PDTEN\_D[31..25] = D64.BS[4].CS[39..32]$
5	47..40	$PDTEN\_A[24..16] = \sim D16.BS[5].CS[47..40]$
		$PDTEN\_B[24..16] = (D16+D64).BS[5].CS[47..40]$
		$PDTEN\_C[24..16] = (D32+D64).BS[5].CS[47..40]$
		$PDTEN\_D[24..16] = D64.BS[5].CS[47..40]$
6	55..48	$PDTEN\_A[15..8] = (D8+D64).BS[6].CS[55..48]$
		$PDTEN\_B[15..8] = \sim D8.BS[6].CS[55..48]$
		$PDTEN\_C[15..8] = (D32+D64).BS[6].CS[55..48]$
		$PDTEN\_D[15..8] = D64.BS[6].CS[55..48]$
7	63..56	$PDTEN\_A[7..0] = BS[7].CS[63..56]$
		$PDTEN\_B[7..0] = \sim D8.BS[7].CS[63..56]$
		$PDTEN\_C[7..0] = (D32+D64).BS[7].CS[63..56]$
		$PDTEN\_D[7..0] = D64.BS[7].CS[63..56]$

Table 4



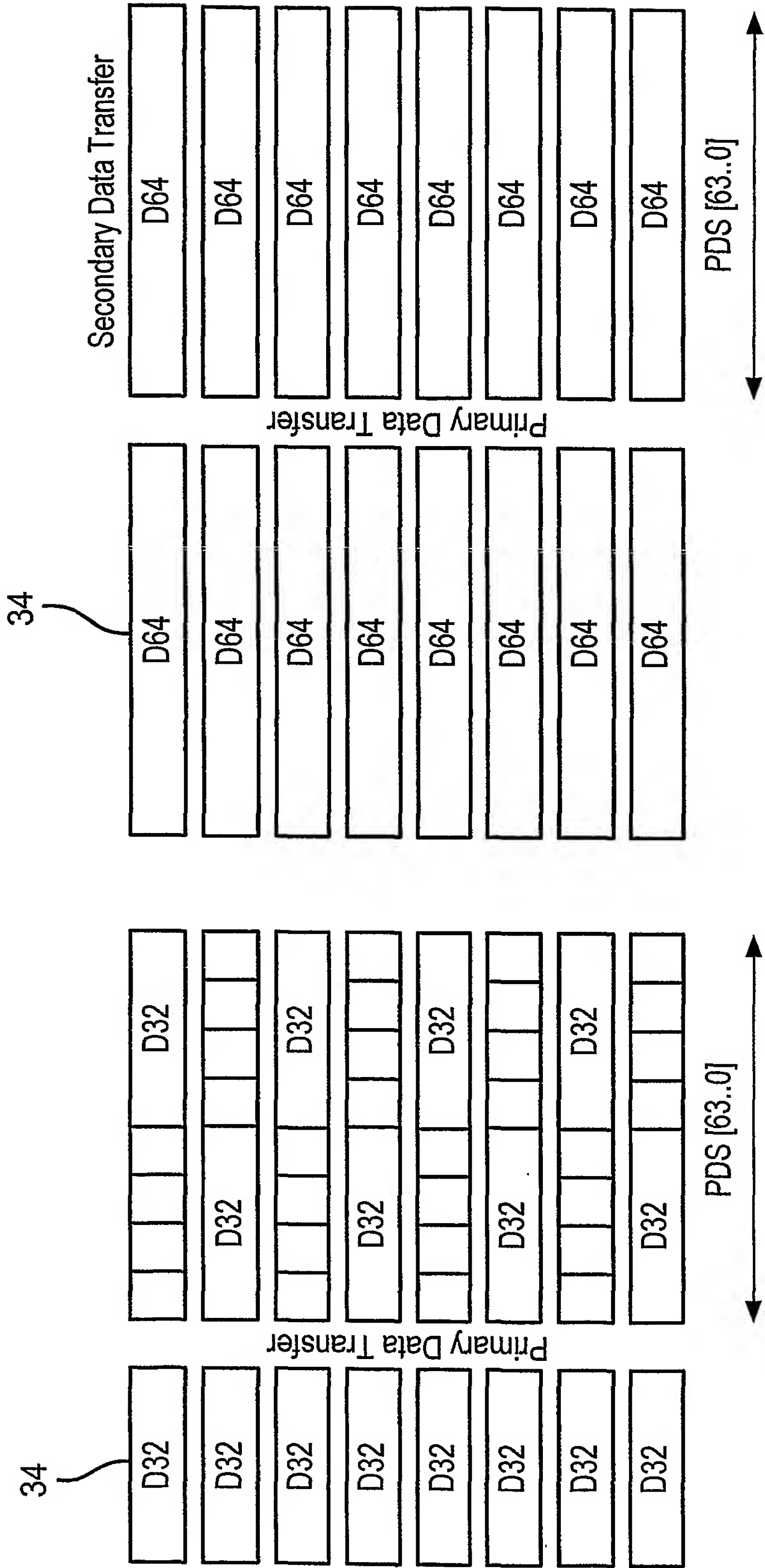


Fig.9 (c)

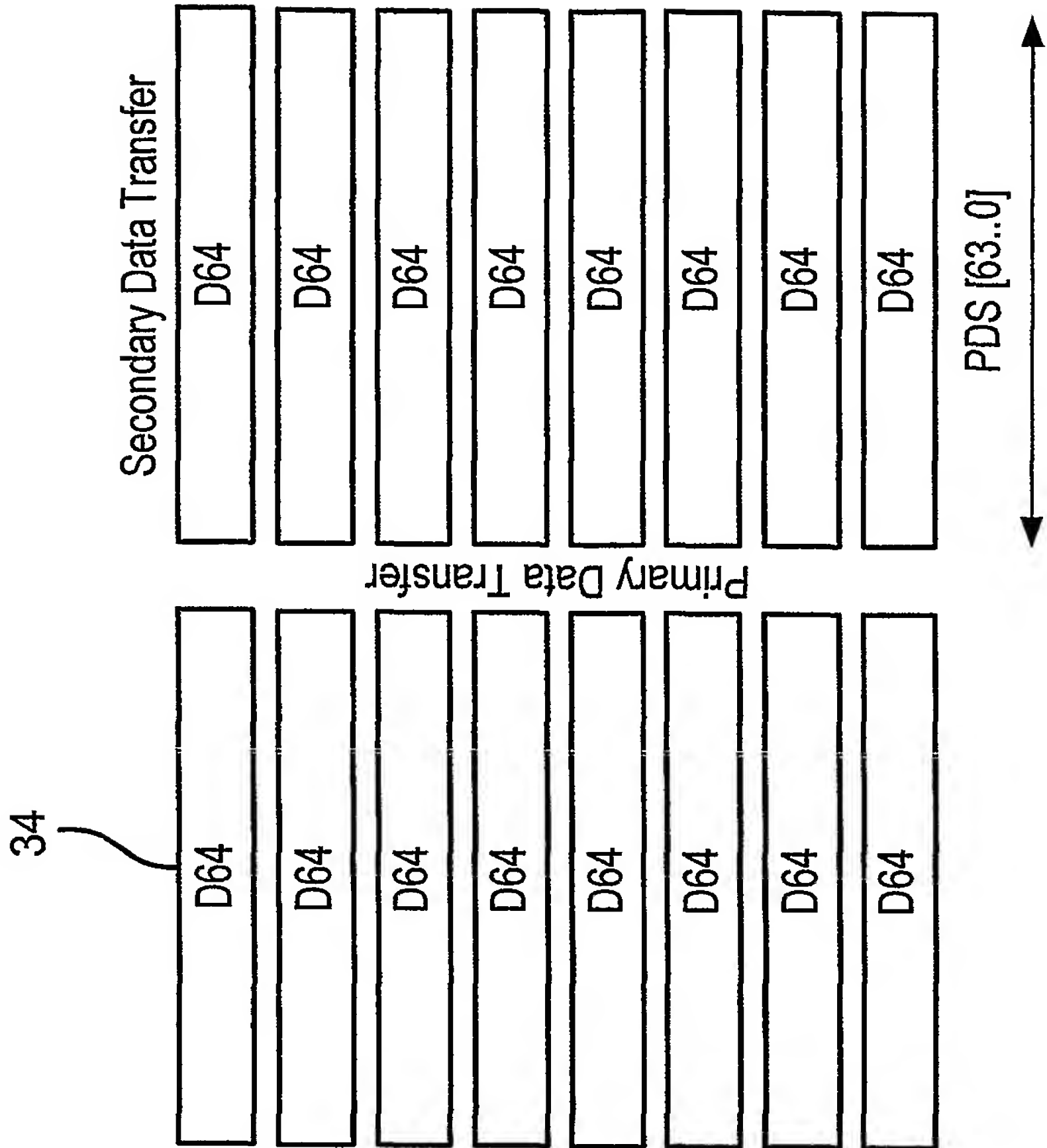


Fig.9 (d)